Abstract of the Disclosure

Deinterlacing apparatus and method capable of outputting two consecutive deinterlaced frames include a field buffer, a shift buffer, a frame generator, and a line exchanger. The field buffer receives and stores a plurality of consecutive interlaced fields, and then outputs, in response to a control signal, p-th interlaced line data of an m-th field, p-th interlaced line data of an (m+2)-th field, p-th interlaced line data of an (m+1)-th field, and (p+1)-th interlaced line data of the (m+1)-th field in series or the p-th interlaced line data of the (m+1)-th field, p-th interlaced line data of an (m+3)-th field, the p-th interlaced line data of the (m+2)-th field, and (p+1)-th interlaced line data of the (m+2)-th field in series. The shift buffer which receives signals output from the field buffer in series, converts the signals into parallel signals, and outputs first through fourth line data in parallel. The frame generator which receives the first through fourth line data from the shift buffer, senses motion in the first through fourth line data between fields, and selectively outputs the result of temporally filtering adjacent line data or the result of spatially filtering adjacent line data in response to the result of the motion sensing. The line exchanger receives the first line data of the shift buffer and an output signal of the frame generator and selectively exchanges the first line data with line data of the output signal of the frame generator in response to a line exchange signal.

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